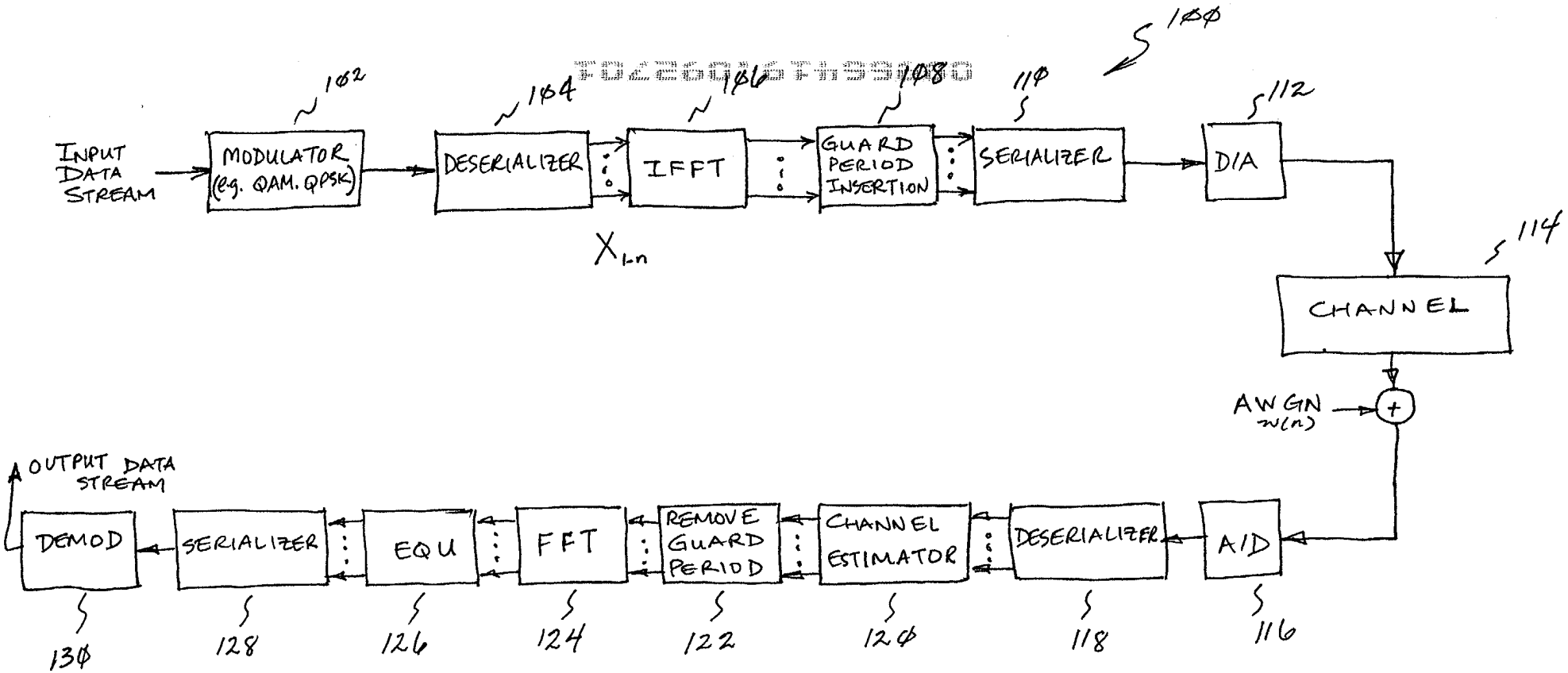
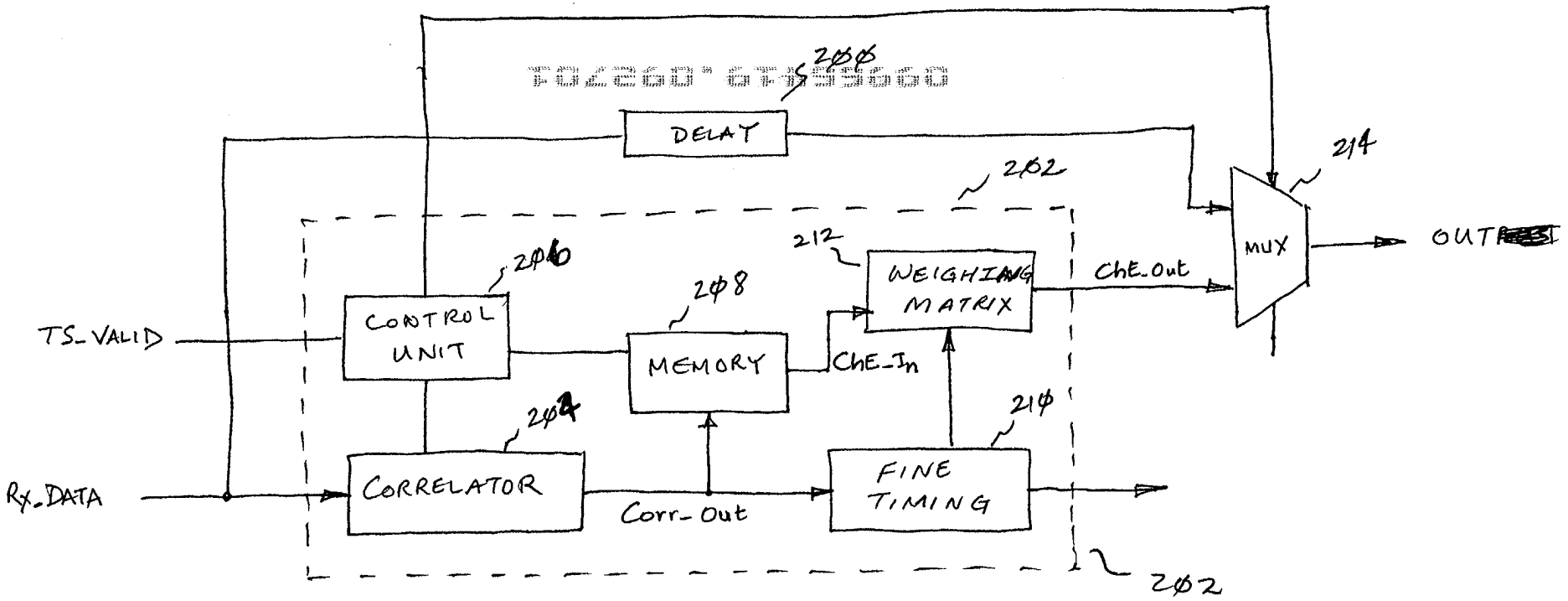
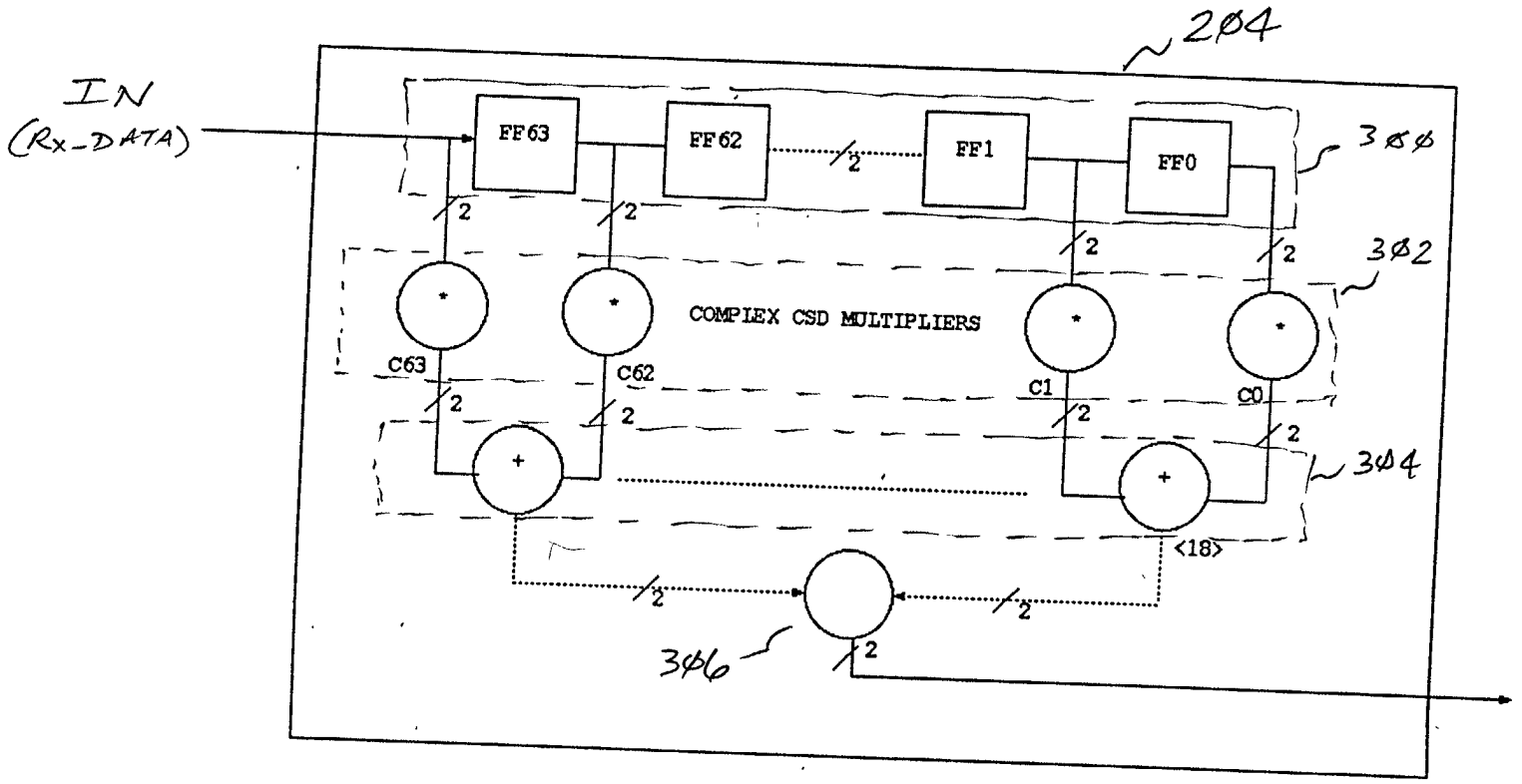

100
102
104
106
108
110
112
114
INPUT DATA STREAM
MODULATOR (eg. QAM. QPSK)
DESERIALIZER
IFFT
GUARD PERIOD INSERTION
SERIALIZER
D/A
CHANNEL
AWGN w(n)
A/D
DESERIALIZER
CHANNEL ESTIMATOR
REMOVE GUARD PERIOD
FFT
EQU
SERIALIZER
DEMOD
OUTPUT DATA STREAM
$X_{l,n}$
116
118
120
122
124
126
128
130


200
DELAY
202
214
MUX
OUTPUT
212
WEIGHING MATRIX
Che_Out
206
CONTROL UNIT
208
MEMORY
Che_In
TS_VALID
204
CORRELATOR
210
FINE TIMING
Corr_Out
RX_DATA
202

-FIGURE 2-

PLACED ETHICS



- FIGURE 3 -